

No.1774E

A7530N

unit

IF Signal Processing (VIF+SIF) Circuit for TV / VCR Use

The LA7530N is an IC containing the VIF section and SIF section on a single chip in the DIP20S package. The use of the small-sized package serves to make VTR tuner units smaller.

As compared with the LA7530N is provided with 2 pins for IF AGC, permitting higher AGC speed. The LA7530N can substitute for the LA7530, but the LA7530 cannot substitute for the LA7530N. For 9V supply, use the LA7533.

Functions

· VIF section: VIF AMP, VIDEO DET, PEAK IF AGC, B/W NOISE CANCELLER, RF AGC, AFT,

VIDEO MUTE.

· SIF section: SIF LIMITER AMP, FM DET, SND MUTE.

Features

- · High-gain VIF amp requiring no preamp.
- · Higher AGC speed.
- · Adjustment-free FM detector because of ceramic discriminator-used quadrature detection.
- Possible to mute video, sound for VTR.
- · Small-sized package.
- · Minimum number of external parts required.

Maximum	Ratings	at $Ta = 25^{\circ}C$
MANUALIM	тиаищев	at 1a – 20 C

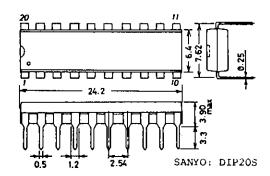
Maximum Ratings at $Ta = 25$ °C			unit	
Maximum Supply Voltage	$ m V_{CC}$ max		14	V
Flow-out Current	I ₁₆ max		5	mA
Maximum Applied Voltage	$ m V_{20}max$		$ m V_{CC}$	V
Allowable Power Dissipation	Pd max	Ta≦40°C	1.1	W
Operating Temperature	\mathbf{Topr}		-20 to +70	$^{\circ}\mathrm{C}$
Storage Temperature	Tstg		-55 to + 125	°C :

Operating Conditions at To = 25°C

operating Conditions at 1a = 29	U	
Recommended Supply Voltage	V_{CC}	12
Operating Voltage Range	V _{CC} op	9 to 13.2

Package Dimensions

(unit:mm) 3021B

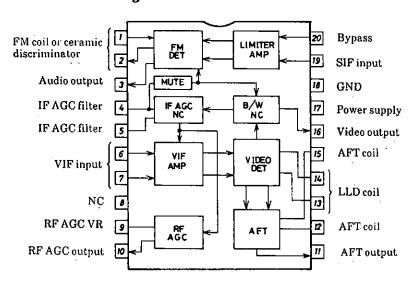


Operating Characteristics at Ta=25°C, V _{CC} =12V, fp=58.75MHz, fs=54.25MHz(VIF),						
	=4.5MHz(SIF)		min		max	unit
Total Circuit Current	<u>I</u> ₁₇	DC	47	58	74	mA
Maximum RF AGC Voltage	\mathbf{v}_{10H}	DČ	8.5	8.9	$9.\overline{2}$	V
Minimum RF AGC Voltage	Vint.	DC		• • • •	0.5	Ÿ
Quiescent Video Output Voltage	V 16	DC	5.7	6.1	6.5	Ý
Quiescent AFT Ouptut Voltage	\mathbf{v}_{11}^{2}	DC	4.5	6.5	7.5	Ÿ
Input Sensitivity	Vi	fm = 400Hz, 40%AM,	30	36	42	dΒμ
		$V_0 = 0.8V_{p-p}$				F -
AGC Range	GR	fm = 400 Hz, $40% AM$,	57	65		dB
		$V_0 = 0.8V_{p-p}$				
Maximum Allowable Input	Vi max	fm = 15kHz, $78%AM$,	100	200		mVrms
		$V_0 = \pm 1 dB$				
Video Output Amplitude	Vo(VIDEO)	Vi = 10 mVrms,	1.9	2.2	2.5	V_{p-p}
		fm = 15kHz, $78%AM$				• •
Output S/N	S/N	Vi=10mVrms CW	4 8	54		dB
Carrier Leak	\mathbf{CL}	Vi = 100 mVrms,	50	55		$d\mathbf{B}$
3.5		fm = 15kHz, $78%AM$				
Maximum AFT Voltage	V_{11H}	Vi=10mVrms CW SWEEP	11	11.4		V
Minimum AFT Voltage	V _{11L} Sf	Vi=10mVrms CW SWEEP		0.5	1.0	V
AFT Detection Sensitivity		Vi=10mVrms CW SWEEP	80	110	150	mV/kHz
White Noise Threshold Level	$\mathbf{v}_{\mathbf{wrh}}$	Vi=10mVrms SWEEP	6.4	6.8	7.2	V
White Noise Clamp Level	Vwci.	Vi=10mVrms SWEEP	4.2	4.6	5.0	V
Black Noise Threshold Level	V_{BTH}	Vi=10mVrms SWEEP	2.1	2.4	2.7	V
Black Noise Clamp Level	V _{BCL} Vo (SIF)	Vi=10mVrms SWEEP	3.8	4.2	4.6	V
SIF Output Signal Voltage	Vo (SIF)	P/S = 20dB	80	140	210	mVrms
Frequency Characteristic	$\mathbf{f_{C}}_{-}$	−3dB	5	7		MHz
Differential Gain	DG	Vi = -27dBm (peak) 87.5%		3		%
T3100 3 T3		VIDEOMOD				
Differential Phase	DP	Vi = -27dBm (peak) 87.5%		3		deg
T 175 1		VIDEOMOD				Ū
Input Resistance	Ri		1.0	1.5	2.0	${f k}\Omega$
Input Capacitance	Ci	_		3.0	6.0	рF
SIF Limiting Voltage	Vi (lim)	_3dB		200	500	μVrms
Detection Output Voltage	Vo (DET)	Vi = 100 mVrms,fm = 400 Hz,	450	680	850	mVrms
M + 1.77		$\Delta f = \pm 25 \text{kHz}$				
Total Harmonic Distortion	THD (DET)	Vi = 100 mVrms,fm = 400 Hz,		0.5	1.3	%
434D :		$\Delta f = \pm 25 \text{kHz}$				
AM Rejection	AMR	Vi = 100 mVrms, fm = 400 Hz,	50	60		ďΒ
		$\Delta f = \pm 25 \text{kHz}, 30\% \text{AM}$				

- Usage Note: 1. Protective circuits must be inserted when using this IC with lines directly connecting the IC pins to external circuits.

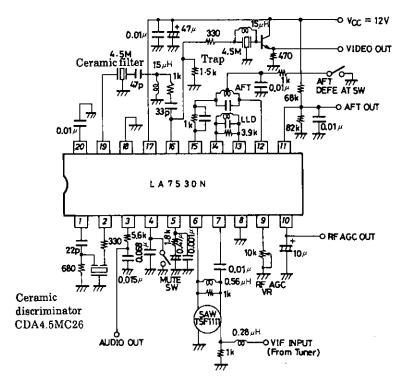
 (For example, this applies to pins 12 and 15.)
 - 2. A 1000pF capacitor must be connected between either pin 5 and ground or between pin 5 and pin 8 to prevent VIF amplifier oscillation.

Equivalent Circuit Block Diagram



Sample Application Circuit (Japan)

* The LA7530N differs from the LA7530 in the circuit externaly connected to pins 5, 8



Unit (resistance: Ω , capacitance: F)

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